

Notice of References Cited	Application/Control No. 10/751,257	Applicant(s)/Patent Under Reexamination SUNG, KWON O.	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,854,115	12-1998	Gardner et al.	438/305
	C	US-6,168,998	01-2001	Park, Jeong-Soo	438/283
	D	US-6,703,269	03-2004	Brown et al.	438/199
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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	U	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 244-45.
	V	Callister, Materials Science and Engineering, 1997, John Wiley & Sons, 4th ed., pp. 605-609.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.